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Amendments to the Specification

Please amend paragraphs [0083] and [0084] in the following manner:

[0083] When the embodiment shown in FIG. 5 is applied to FIG. 2, the quantization execution module 10 performs the DCT transformation and outputs the quantized data into the 8 times 8 block register 21. At the same time, the search device 40 receives the data to transfer it to the search control device 24 which is prepared to inversely zigzag scan the block register to search for a [[the]] valid coefficient.

[0084] The 8 times 8 block register 21 receives the data and latches the data into each block of the registers. At the same time, the search control device 24 performs the inverse zigzag scan to start searching for the valid coefficient with 1 value. Prior to this process, the correction level is required to be set to the correction level setup register 22. In addition, the valid coefficient address may be moved to other address.

Please amend paragraph [0093] in the following manner:

[0093] The flowchart of the embodiment shown in FIG. 4 is designed to quantize the 8 times 8 pixel blocks and then transmit or receive data. However, in this embodiment, the above-mentioned flowchart is redesigned to quantize an 8 times 8 matrix for sound signals and then transmit or receive data. Subsequently, in FIG. 5, the zigzag scan is performed to search for the valid coefficient, followed by a modification of the coefficient from 1 to 0 and a replacement of the coefficient. As a result, the amount of coding is reduced without much affecting the sound. In addition, compression processing time is further reduced.